

01/28/00  
JCS25 U.S. PTO

**NEW UTILITY PATENT APPLICATION  
TRANSMITTAL**

(Only for new nonprovisional applications under 37 C.F.R. 1.53(b))

Docket No.  
M4065.0225/P225

Total pages in this  
submission  
212

JCS25 U.S. PTO  
01/28/00  
0093663

**TO THE ASSISTANT COMMISSIONER FOR PATENTS**

Box Patent Application  
Washington, D.C. 20231

Transmitted herewith for filing under 35 U.S.C. 111(a) and 37 C.F.R. 1.53(b) is a new utility patent application for an invention entitled:

PATTERN RECOGNITION WITH THE USE OF MULTIPLE IMAGES

and invented by:

Douglas D. Do

**IF A CONTINUATION APPLICATION**, check appropriate box and supply requisite information:

Continuation       Divisional

Continuation-in-part (CIP) of prior application No.: \_\_\_\_\_

Enclosed are:

**Application Elements**

1.  Filing fee as calculated and transmitted as described below
2.  Specification having 28 pages(s) and including the following:
  - a.  Descriptive title of the invention
  - b.  Cross references to related applications (*if applicable*)
  - c.  Statement regarding Federally-sponsored research/development (*if applicable*)
  - d.  Reference to microfiche appendix (*if applicable*)
  - e.  Background of the invention
  - f.  Brief summary of the invention
  - g.  Brief description of the drawings (*if drawings filed*)
  - h.  Detailed description
  - i.  Claims as classified below
  - j.  Abstract of the disclosure

**Application Elements (continued)**

3.  Drawing(s) (*when necessary as prescribed by 35 U.S.C. 113*)  
 Formal       Informal      Number of sheets: 11

4.  Oath or Declaration

- a.  Newly executed (original or copy)     Unexecuted
- b.  Copy from a prior application (37 C.F.R. 1.63(d)) (*for continuation/divisional applications only*)
- c.  With Power of Attorney       Without Power of Attorney

5.  Incorporation by reference (*usable if Box 4b is checked*)  
The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.

6.  Computer program in microfiche

7.  Genetic sequence submission (*if applicable, all must be included*)

- a.  Paper copy
- b.  Computer readable copy
- c.  Statement verifying identical paper and computer readable copies

**Accompanying Application**

8.  Assignment papers (*cover sheet & document(s)*)

9.  37 C.F.R. 3.73(b) statement (*when there is an assignee*)

10.  English translation document (*if applicable*)

11.  Information Disclosure Statement/PTO-1449       Copies of IDS citations

12.  Preliminary Amendment

13.  Acknowledgment postcard

14.  Certified copy of priority document(s) (*if foreign priority is claimed*)

15.  Certificate of Mailing  
 First Class       Express Mail (Label No.: \_\_\_\_\_ )

16.  Small Entity statement(s) -- # submitted \_\_\_\_\_ (*if Small Entity status claimed*)

**Accompanying Application (continued)**

17.  Additional enclosures (please identify below):

**Fee Calculation and Transmittal**

The filing fee for this utility patent application is calculated and transmitted as follows:

Large Entity

Small Entity

<b><u>CLAIMS AS FILED</u></b>					
<b>For</b>	<b># Filed</b>	<b># Allowed</b>	<b># Extra</b>	<b>Rate</b>	<b>Fee</b>
<b>Total Claims</b>	39	- 20 =	19	x \$18.00	\$342.00
<b>Independent Claims</b>	6	- 3 =	3	x \$78.00	\$234.00
<b>Multiple Dependent Claims (check if applicable)</b> <input type="checkbox"/>					
<b>Other Fees (specify purpose):</b> Recordation Form Cover Sheet					\$40.00
<b>BASIC FEE</b>					\$690.00
<b>TOTAL FILING FEE</b>					\$1,306.00

A check in the amount of \$1,306.00 to cover the total filing fee is enclosed.

The Commissioner is hereby authorized to charge and Deposit Account No. 4 - 1073 as described below. A duplicate copy of this sheet is enclosed.

Charge the amount of \_\_\_\_\_ as filing fee.

Credit any overpayment.

Charge any additional filing fees required under 37 C.F.R. 1.16 and 1.17.

Charge the issue fee set in 37 C.F.R. 1.18 at the mailing of the Notice of Allowance, pursuant to 37 C.F.R. 1.31(b).



Dated: January 28, 2000

Thomas J. D'Amico

Attorney Reg. No.: 28,371

Dickstein Shapiro Morin & Oshinsky LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

PATENT  
Docket No.: M4065.0225/P225  
Micron Docket No.: 99-0079

PATTERN RECOGNITION WITH THE USE OF MULTIPLE IMAGES

Inventor:  
Douglas D. Do

## PATTERN RECOGNITION WITH THE USE OF MULTIPLE IMAGES

### BACKGROUND OF THE INVENTION

#### 1. FIELD OF THE INVENTION

The present invention relates to the field of semiconductor fabrication  
5 and, more particularly to a method and apparatus that uses multiple images in a pattern recognition process used to detect defects in the manufacture of a semiconductor device.

#### 2. DESCRIPTION OF THE RELATED ART

In the semiconductor industry, there is a continuing movement towards  
10 higher integration, density and production yield, all without sacrificing throughput or processing speed. The making of today's integrated circuits (ICs) requires a complex series of fabrication, inspection and testing steps interweaved throughout the entire process to ensure the proper balance between throughput, processing speed and yield. The inspections and tests are designed to detect unwanted  
15 variations in the wafers produced, as well as in the equipment and masks used in the fabrication processes. One small defect in either the devices produced or the process itself can render a finished device inoperable.

Many of the inspection steps once done manually by skilled operators have been automated. Automated systems increase the process efficiency and  
20 reliability as the machines performing the inspection are more consistent than human operators who vary in ability and experience and are subject to fatigue when performing repetitive tasks. The automated systems also provide greater amounts of data regarding the production and equipment, which enables process engineers to both better analyze and control the process.

One such automated inspection step is known as pattern recognition or pattern inspection. Many different "patterns" appear on both the wafer and the masks used to produce the ICs. Typical pattern inspection systems are image based, as described, for example, in U.S. Patent Nos. 4,794,646; 5,057,689; 5,641,960; 5 and 5,659,172. In Patent No. 4,794,646, for example, the wafer, or part thereof, is scanned and a highly resolved picture or image of the pertinent "pattern" is obtained. This pattern image is compared to other pattern images retrieved from the same or other wafers, or is compared to an ideal image stored in the inspection system database. Differences highlighted in this comparison identify possible defects 10 in the IC or wafer.

Fig. 1 illustrates the conventional pattern recognition method 10 currently performed by today's pattern recognition or pattern inspection tools. The method 10 begins when a user places a wafer or other object to be inspected into the inspection apparatus (step 12). After scanning the wafer, the apparatus displays a 15 "field of view" containing images from a portion of the scanned wafer (step 14). These images are to be inspected by the apparatus and thus, are referred to herein as the "inspected images." The apparatus then displays a single pattern box within the field of view (step 16). This pattern box will be used by an operator of the apparatus to select a desired image from the inspected image. The selection is made 20 by placing the pattern box over an image currently displayed in the field of view (step 18). The apparatus "learns" the pattern of the selected image and subsequently uses the learned pattern in a pattern recognition analysis to determine if the wafer has any defects (step 20). The use of "learns" or "learned" herein refers to the process of obtaining pattern information for the selected image and storing

the information for subsequent use in a pattern recognition or critical dimension (CD) analysis. The process of learning a pattern and performing a pattern recognition analysis is well known and can be carried out in any known manner.

The method 10, however, is not without its shortcomings. Referring to Fig. 2, exemplary inspected images 50 are illustrated. In this example, the images 50 are contacts formed within a wafer being inspected. Each image 50 contains a top surface 52 of the contact and a bottom surface 54 of the contact. The bottom surfaces 52 are the desired features, which must be inspected for defects. Typical defects include under etching and over etching of the contacts and what is sometimes referred to as “closed contacts,” which are partially etched contacts.

As can be seen from Fig. 2, the top surfaces 52 are larger and much more prominent than the bottom surfaces 54. Often times, the inspection apparatus receives such strong signals from the contact top surfaces 52 that it is difficult to detect and properly inspect the contact bottom surfaces 54, which, as described above, are the desired features. By way of example, it is presumed that during the method 10 (Fig. 1) the operator placed a pattern box 70 over an image comprised of contact images 80. The selected image and its pattern found within box 70 will be hereinafter referred to as the “pattern to recognize.” Like the images 50 to be inspected, the images 80 of the pattern to be recognized contain top surfaces 82 and bottom surfaces 84. In this example, the user selected pattern to be recognized contains three contact bottom surfaces 84, each with their own expected or desired shape. The user selected pattern to be recognized also contains portions of three top surfaces 82 that are much larger than the bottom surfaces 84. The inspection apparatus learns the pattern to be recognized, which includes large signals associated

with the top surfaces 82, and subsequently uses the learned pattern for comparison with the inspected images, the apparatus detects three matches 60, 62, 64.

As can be seen from Fig. 2, the three declared matches 60, 62, 64 do not contain bottom surfaces 54 that match the bottom surfaces 84 of the pattern to be recognized. Moreover, some of the matches 60, 62, 64 contain defects, e.g., under etched bottom surfaces 54a, 54b, 54c. Thus, the apparatus has incorrectly detected three matches 60, 62, 64, when there should have been zero matches and more importantly, the apparatus failed to detect three defects 54a, 54b, 54c. This anomaly occurs since the apparatus receives such strong signals from the much larger and much more prominent top surfaces 52, 82, which substantially match each other. By contrast, the apparatus receives weaker signals from the much smaller and less prominent bottom surfaces 54, 84, which do not match each other and also contain defects. Since there is much more information associated with the top surfaces 52, 82 than the bottom surfaces 54, 84, the apparatus detects the matches 60, 62, 64 based on the top surfaces 52, 82, which results in improper pattern recognition results.

Typically, the inspection apparatus will allow a user to set pattern recognition thresholds. These thresholds are designed to reduce or increase the matching percentage required between the pattern to be recognized and the inspected images. Thus, a user may set a matching threshold to 100%, in which case, the apparatus will only declare matches when the inspected images contain patterns that exactly match the pattern to be recognized. This would ensure that defective images 54a, 54b, 54c are not matched to desired and non-defective images. However, due to variations in the manufacturing process, a matching

threshold of 100% would most likely lead to no desired matches or too few desired matches than are actually present. The apparatus would not detect all of the proper matches, if it detects any at all (i.e., it is under inclusive). On the other hand, if the matching threshold is set too low, e.g., 50%, then too many matches will occur.

5 These matches will include defective images 54a, 54b, 54c whose patterns are within the matching percentage (i.e., it is over inclusive). Typically, the matching threshold is set to approximately 65% to balance between the over inclusive and under inclusive matching thresholds.

Even with a threshold setting of 65%, the conventional pattern

10 recognition process is still unreliable. Fig. 3 illustrates another set of exemplary inspected images 50. Four sample images 90, 92, 94, 96 are also illustrated. The sample images 90, 92, 94, 96 each contain three contact images 50. The first three images 90, 92, 94 contain top surfaces 52 and bottom surfaces 54, while the fourth image 96 only contains bottom surfaces 54. The second and third images 92, 94  
15 also contain defective bottom surfaces 92a, 94a, respectively. Using the current pattern recognition process, the first three images 90, 92, 94 would most likely match each other if one of the images 90, 92, 94 were used as a pattern to be recognized. This would happen even though the bottom surfaces 54 of the images 90, 92, 94 do not match at all and some of the surfaces 92a, 94a are defective.

20 It would be desirable to use the fourth sample image 96 as the pattern to be detected. As noted above, the fourth image 96 does not contain any top surfaces 52. However, the fourth sample image 96, which has bottom surfaces 54 substantially matching the bottom surfaces 54 of the first sample image 90, would not match any of the other images 90, 92, 94 because the other images contain both

top and bottom surfaces 52, 54. Thus, even if it were possible to select the fourth sample image 96 as a pattern to be recognized, the pattern recognition analysis would be corrupted by the top surfaces 52, of the inspected images (e.g., images 90, 92, 94) which are not part of the desired features.

5 Accordingly, there is a desire and need for a pattern recognition process that filters out undesirable features from the object being inspected prior to performing a pattern recognition analysis on the object. There is also a desire and need for a pattern recognition process that allows a user to select multiple desired images of the object being inspected to be used as a pattern to be recognized during  
10 a pattern recognition analysis on the object.

#### SUMMARY OF THE INVENTION

The present invention provides a pattern recognition technique that substantially filters out undesirable features of the object being inspected prior to performing a pattern recognition analysis on the object.

15 The present invention also provides a pattern recognition technique that allows multiple desired images of the object being inspected to be used as a pattern to be recognized during a pattern recognition analysis on the object.

The above and other features and advantages of the invention are achieved by a pattern inspection apparatus and method that uses multiple images in a  
20 pattern recognition process used to detect defects in an object being inspected. A user is provided with multiple image selection windows allowing the user to select multiple desired images from the object to form a pattern to be recognized within the object. The multiple desired images will be substantially free from undesired features of the object. Once the multiple desired images are selected, the

relationship between them is determined and used to learn the pattern to be recognized. The relationships between the desired images further filters out undesired features. The pattern to be recognized is used in a subsequent pattern recognition analysis. Since the pattern to be recognized includes only desired 5 images and their relationship, undesired features that could corrupt the pattern recognition analysis are not present during the analysis. Thus, the apparatus and method are more accurate than prior inspection tools.

#### BRIEF DESCRIPTION OF THE DRAWINGS

The foregoing and other advantages and features of the invention will 10 become more apparent from the detailed description of the preferred embodiments of the invention given below with reference to the accompanying drawings in which:

Fig. 1 illustrates in flowchart form a conventional pattern recognition process;

Fig. 2 illustrates exemplary inspected images and a pattern to be 15 recognized used in the process illustrated in Fig. 1;

Fig. 3 illustrates exemplary inspected images used in the process illustrated in Fig. 1;

Fig. 4 illustrates a pattern inspection apparatus constructed in accordance with the present invention;

20 Fig. 5 illustrates in flowchart form an exemplary pattern recognition method using multiple images in accordance with a first embodiment of the present invention;

Figs. 6a and 6b illustrate exemplary images to be detected and their relationships;

Fig. 7 illustrates exemplary inspected images and a pattern to be recognized used in the processes illustrated in Figs. 5 and 8;

Fig. 8 illustrates in flowchart form another exemplary pattern recognition method using multiple images in accordance with a second embodiment of the  
5 present invention; and

Figs. 9a-9e illustrate sample field of view images and a resultant pattern to be recognized in accordance with any of the embodiments of the present invention.

#### DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

In an exemplary implementation, a pattern inspection or defect detection  
10 apparatus 100, as shown in Fig. 4, can be constructed having a scanning electron-beam microscope (SEM) 102 used for viewing purposes, as is well known in the art. Although the invention is described herein as used during final wafer inspection of the IC dies, it can readily be appreciated that the invention has application to any other stage of manufacturing, e.g., inspection after the initial photomasking and  
15 baking of a wafer known as "development inspection," where critical dimension measurements or pattern inspections are contemplated.

The SEM 102 is provided with an object support structure 108 in the form of a stage or chuck, which may be moveable or stationary. An object 120 under evaluation such as an IC sample die or a wafer containing many IC dies rests  
20 on support structure 108. Under control of computer 110, the structure 108 may be moveable in two (X-Y) or three (X-Y-Z) dimensions to facilitate the proper viewing of the object 120 (or parts thereof). A deflector 104 and detector 106, whose operations will be described in detail below, are also provided within the SEM 102 to assist in the viewing of the object 120. An image processor 112, together

with its accompanying image memory 114 are provided to process the image signals output by the SEM 102 and transform the signals into visual representations or data which can be viewed on a display monitor 116 (e.g., cathode ray tube (CRT)) or used for processing in computer 110.

5 In operation, the SEM 102 uses a finely focused electron beam directed by deflector 104, preferably under the control of the computer 110, to scan the surface of the object 120 resting on the support 108, typically in two dimensions (X-Y). For the purposes of discussion only, it will be assumed herein that the object 120 under evaluation is a silicon wafer having a plurality of contacts formed therein, 10 such as contacts 50 illustrated in Figs. 2, 3 and 8. The electrons striking the semiconductor surface of the object 120 collide with inner shell electrons of the object atoms causing inelastic collisions of low energy emitting so-called "secondary electrons" which are serially detected by the detector 106.

The detected electron current is output as an image signal to the 15 computer 110 and image processor 112 where an image representative of the surface of the object 120 can be formed based on the image signal. This image may be stored in the image memory 114 and can be viewed on the monitor 116 or otherwise processed by the computer 110. The high resolution of the image is attributed to the small diameter (e.g., several nanometers) of the electron beam 20 illuminator. The visual contrast achieved in the image originates mostly from variations in the extent of the secondary electron emissions from the topographic features of the surface of the object 120.

Fig. 5 illustrates an exemplary method 200 of using multiple images in a pattern recognition process constructed in accordance with an embodiment of the

present invention. The method 200 (with the exception of step 202, described below) is preferably implemented in software and executed in the computer 110 of the pattern inspection apparatus 100 illustrated in Fig. 4. It should be noted that the method 200 of the present invention may also be implemented in a conventional 5 CD-SEM apparatus such as the "IVS-200" made by IVS, Inc., the "Opal 7830si" made by Applied Materials, or the "S-8820/8620" made by Hitachi, by modifying the computer program used by the control computer within the CD-SEM apparatus such that the apparatus implements the operations of the method 200 (described below).

10 Referring to Figs. 4 and 5, the method 200 begins when a user places a wafer or other object 120 to be inspected into the inspection apparatus 100 (step 202). It should be appreciated that the object 120 can be a semiconductor wafer at any stage of the manufacturing process or it can be a reticle used to create a mask, such as a phase shifting mask and that the invention should not be limited solely to 15 wafers. However, to remain consistent with the preceding discussion, the object 120 will be a wafer having contacts formed therein. After scanning the wafer, the method 200 displays on the display 116 a field of view containing images from a portion of the scanned wafer (step 204). As noted above with reference to Fig. 1, these images are to be inspected by the apparatus 100 and thus, are referred to 20 herein as the "inspected images."

The apparatus 100 then prompts the user to input the number of image selection windows required (step 206). Each image selection window is used by the method 200 to select an image, within the inspected images, to be used as part of the pattern to be detected. In the prior art method one large box is used to obtain

an image having both desired (e.g., bottom surfaces of contacts) and undesired features (e.g., top surfaces of contacts). The present method 200 uses multiple sizable image selection windows. This way, instead of placing one large box over an image containing both desired and undesired features, the method 200 uses several 5 multiple sizable image selection windows to select images with only desired features.

As will become apparent below, this allows the method 200 to substantially filter out undesired features from the pattern to be recognized and thus, the pattern recognition analysis.

At step 208, the user inputs the number of desired windows. By allowing 10 the user to select more than one image selection window, the method 200 of the present invention allows multiple different images to be used in the pattern recognition analysis. It should be noted that these steps differ from the prior art, which only provides the user with one box, and thus, does not allow the user to select multiple images (see step 16 of Fig. 1). Moreover, the prior art does not filter 15 out undesired features.

Referring also to Figs. 9a-9e, the apparatus 100 displays the appropriate number of image selection windows 300, 302, 304 within the field of view (step 210). Figs. 9a-9e illustrate an example where three image selection windows 300, 302, 304 have been chosen by a user at step 208. These windows 300, 302, 304 are 20 used to select desired images 310a, 310b, 310c from the inspected images. The selection is made by placing each image selection window 300, 302, 304 over a separate desired image 310a, 310b, 310c that is currently displayed in the field of view (step 212). It should be understood that depending upon how the method 200 is implemented, the user may be required to hit a “select” or enter “button”

after positioning and sizing the image selection windows 300, 302, 304 to select the images 310a, 310b, 310c. By using multiple image selection windows 300, 302, 304, the user can size them so that they only select desired features (e.g., bottom surfaces 54). That is, it is possible for the user to substantially filter out unwanted features, such as the top surfaces 52 of the contacts, from the pattern to be recognized at this step.

At step 214, the apparatus 100 learns the selected images and the relationship between them. That is, the apparatus 100 obtains and stores information for the selected images. The information can be stored in the memory 114 or other computer readable medium connected to or contained within the apparatus 100. The spatial relationship between the selected images is also obtained and stored. Learning the spatial relationship between the images will further filter out undesired features. This can best be illustrated by the following example.

Referring to Fig. 6a, five exemplary images 250, 252, 254, 256, 258 are illustrated.

These images 250, 252, 254, 256, 258 correspond to potential desired images within the inspected images. That is, these images 250, 252, 254, 256, 258 could represent the bottom surfaces of various contacts found on the object 120.

Referring to Fig. 6b, it can be seen that a pattern 260 to be detected has been selected by a user. The pattern 260 consists of three selected images 250, 254, 256, the spatial relationship 266, 268 between image 250 and image 254 and the spatial relationship 262, 264 between image 250 and image 256. It is desired that the relationships 262, 264, 266, 268 be two or three dimensional vectors. It should be appreciated, however, that any means for indicating the relationship between the selected images can be used. Another indicia of the relationship between the images

could include the distance from an origin or test point on the object 120 (not shown). Fig. 9e illustrates the pattern 310 to be recognized, the spatial relationships 312, 314 between images 310a and 310c and the spatial relationship between image 310a and 310b.

5 Referring again to Figs. 4 and 5, once the images and their relationship are learned, the method 200 continues at step 216, where the pattern to be recognized is learned and subsequently used a pattern recognition analysis to determine if the object 120 has any defects. Here, unlike the prior art method 10 (Fig. 1), the learned pattern includes the learned information of the multiple images, 10 the relationship between the images and any other information required by the apparatus 100 to perform the pattern recognition analysis. Any required pattern information can be stored in the memory 114 or other computer readable medium connected to or contained within the apparatus 100. At this point, the process of learning the pattern and performing the pattern recognition analysis can be carried 15 out in any known manner.

Fig. 7 illustrates the exemplary inspected images 50 described above with reference to Fig. 2. However, a new pattern 310 to be recognized is illustrated. As illustrated in Figs. 9a-9e, the pattern 310 was created using the method 200 (Fig. 6) of the present invention. Unlike the pattern contained within box 70 of Fig. 2, the 20 pattern 310 created in accordance with the present invention consists solely of images of the bottom surface 384 of the contacts. Each bottom surface 384 was individually selected by respective pattern windows 300, 302, 304. As described above, the present invention learns the individual images within these windows 300, 302, 304 and their relationships (Fig. 9e). By learning the images 384 and their

relationship, the learned pattern 310 to be recognized is devoid of any undesired features, particularly the top surfaces 52, 82 (illustrated in Fig. 2). As such, the present invention using pattern 310 detects zero matches from the inspected images 50. This is the correct result, since none of the bottom surfaces 54 within the 5 inspected images match the bottom surfaces 384 of the pattern 310 to be recognized.

Fig. 8 illustrates another exemplary method 400 of using multiple images in a pattern recognition process constructed in accordance with another embodiment of the present invention. The method 400 (with the exception of step 10 402, described below) is preferably implemented in software and executed in the computer 110 of the pattern inspection apparatus 100 illustrated in Fig. 4. As with the method 200 (Fig. 5), the method 400 may also be implemented in a conventional CD-SEM apparatus such as the "IVS-200" made by IVS, Inc., the "Opal 7830si" made by Applied Materials, or the "S-8820/8620" made by Hitachi, 15 by modifying the computer program used by the control computer within the CD-SEM apparatus such that the apparatus implements the operations of the method 400 (described below).

Referring to Figs. 4 and 8, the method 400 begins when a user places a wafer or other object 120 to be inspected into the inspection apparatus 100 (step 20 402). After scanning the wafer, the method 400 displays on the display 116 a field of view containing images from a portion of the scanned wafer (step 404). As noted above, these images are referred to as the inspected images. The apparatus 100 then displays an image selection window within the field of view (step 406). At step 408 the user uses the window to select a desired image from the inspected images. The

selection is made by placing the image selection window over an image that is currently displayed in the field of view. It should be understood that depending upon how the method 400 is implemented, the user may be required to hit a “select” or enter “button” after positioning and sizing the window to select the 5 image. At step 410, the user is prompted to determine if more image selection windows are required.

If at step 412, it is determined that more image selection windows are required, the method 400 continues at step 406 where a new image selection window is displayed on the field of view. This way, the user may select multiple 10 images using multiple image selection windows. By using multiple image selection windows, the user can size these windows so that they only select desired features. That is, it is possible for the user to substantially filter out unwanted features, such as the top surfaces of the contacts, from the pattern to be recognized.

If at step 412 it is determined that no more image selection windows are 15 required, the method continues at step 414. At step 414, the apparatus 100 learns the selected images and the relationship between them. That is, the apparatus 100 obtains and stores information for the selected images. The information can be stored in the memory 114 or other computer readable medium connected to or contained within the apparatus 100. The spatial relationship between the selected 20 images is also obtained and stored (as described above with reference to Fig. 6). Once the images and their relationship are learned, the method 400 continues at step 416, where the pattern to be recognized is learned and subsequently used a pattern recognition analysis to determine if the object 120 has any defects. Here, unlike the prior art method 10 (Fig. 1), the learned pattern includes the learned

information of the multiple images, the relationship between the images and any other information required by the apparatus 100 to perform the pattern recognition analysis. Any required pattern information can be stored in the memory 114 or other computer readable medium connected to or contained within the apparatus 5 100. At this point, the process of learning the pattern and performing the pattern recognition analysis can be carried out in any known manner.

The present invention improves the pattern recognition process by allowing the user to create a pattern to be recognized using multiple images. By select multiple images, the user can substantially filter out unwanted features, such as 10 the top surfaces of contacts formed in a wafer. More importantly, the present invention uses relationships between the selected images to hone in on the exact pattern to be recognized. Thus, the present invention can perform pattern recognition using the typical thresholds currently used in today's inspection systems, but with substantially better results.

15 It should be appreciated that the learned pattern created by the methods of the present invention can be used in the pattern recognition analysis to detect defects in the object as well as to detect desired (i.e., properly etched) patterns in the object. It should also be appreciated that the learned pattern created by the methods of the present invention can be used in both pattern recognition and 20 critical dimension (CD) analysis processes. Moreover, the methods of the present invention can be used to inspect wafers, reticles or other semiconductor devices requiring pattern inspection/recognition.

While the invention has been described in detail in connection with the preferred embodiments known at the time, it should be readily understood that the

invention is not limited to such disclosed embodiments. Rather, the invention can be modified to incorporate any number of variations, alterations, substitutions or equivalent arrangements not heretofore described, but which are commensurate with the spirit and scope of the invention. Accordingly, the invention is not to be seen as limited by the foregoing description, but is only limited by the scope of the appended claims.

What is claimed as new and desired to be protected by Letters Patent of the United States is:

1. A method of operating an inspection apparatus to inspect a device

used in a manufacturing process, said method comprising the steps of:

scanning the device;

displaying a plurality of images corresponding to respective areas of the

5 device;

selecting at least two of the plurality of displayed images;

deriving a spatial relationship between the selected images; and

forming a pattern to be recognized on the device from the selected

images and the derived spatial relationship.

10

2. The method of claim 1 further comprising the step of storing

information associated with the selected images and the derived spatial relationship

on a computer readable medium of the inspection apparatus.

15

3. The method of claim 2 further comprising using said stored selected

images and the stored derived spatial relationship in a pattern recognition analysis to

detect defects in the device.

4. The method of claim 2 further comprising using said stored selected

20 images and the stored derived spatial relationship in a pattern recognition analysis to

detect desired patterns in the device.

5. The method of claim 1 wherein said selecting step comprises:

displaying at least two image selection windows with the displayed images;  
and

placing the at least two image selection windows over respective displayed  
images.

5

6. The method of claim 5 wherein the image selection windows are  
placed over images corresponding to properly formed areas of the device.

7. The method of claim 5 wherein the image selection windows are  
10 placed over images corresponding to defectively formed areas of the device.

8. The method of claim 5 wherein a user of the apparatus is prompted for  
a number of image selection windows to be displayed.

15 9. The method of claim 1 wherein said selecting step comprises:  
displaying an image selection window with the displayed images;  
placing the image selection window over a displayed image;  
determining if another image selection window is required; and  
if another image selection window is required, repeating said steps of

20 displaying an image selection window, placing the image selection window over a  
displayed image and determining if another image selection window is required.

10. The method of claim 9 wherein the image selection windows are  
placed over images corresponding to properly formed areas of the device.

11. The method of claim 9 wherein the image selection windows are placed over images corresponding to defectively formed areas of the device.

5           12. The method of claim 1 wherein the device is a semiconductor wafer.

13. The method of claim 12 wherein the areas are contacts formed in the wafer.

10          14. The method of claim 13 wherein the contact areas have desired and undesired features and said selecting step comprises:  
              displaying at least two image selection windows with the displayed images;  
              and  
              placing the at least two image selection windows over images associated  
15          with the desired features of the contact areas.

15. The method of claim 13 wherein the contact areas have desired and undesired features and said selecting step comprises:  
              displaying at least two image selection windows with the displayed images;  
20          and  
              placing the at least two image windows over images associated with the undesired features of the contact areas.

16. The method of claim 13 wherein the contact areas have desired and undesired features and said selecting step comprises:

displaying an image selection window with the displayed images;

placing the image selection window over an image associated with the

5      desired feature of a contact area;

determining if another image selection window is required; and

10     if another image selection window is required, repeating said steps of displaying an image selection window, placing the image selection window over an image associated with a desired feature and determining if another image selection

10     window is required.

17. The method of claim 1 wherein the device is a reticle.

18. The method of claim 1 wherein the derived spatial relationship

15    comprises respective spatial relationships between pairs of the selected images.

~~19. A method of inspecting a semiconductor wafer having objects formed therein, said method comprising the steps of:~~

scanning the wafer;

20    displaying a plurality of images corresponding to respective areas in the wafer;

selecting at least two of the plurality of displayed images;

deriving a relationship between the selected images; and

forming a pattern to be recognized on the wafer from the selected images and the derived relationship.

20. The method of claim 19 further comprising the step of storing  
5 information associated with the selected images and the derived relationship on a computer readable medium of the inspection apparatus.

21. The method of claim 20 further comprising using said stored selected images and the stored derived relationship in a pattern recognition analysis to detect  
10 defects in the wafer.

22. The method of claim 20 further comprising using said stored selected images and the stored derived relationship in a pattern recognition analysis to detect desired patterns in the wafer.

15

~~23.~~ A method of inspecting a semiconductor device having objects formed therein, said method comprising the steps of:

scanning the device to obtain scanned object images;  
selecting at least two of the scanned object images; and  
20 forming a pattern to be recognized on the device from the selected images and a spatial relationship between the images.

~~24.~~ An inspection apparatus for use in inspecting a manufacturing device used in a manufacturing process, said apparatus comprising:

a scanning device, said scanning device obtaining images of the manufacturing device;

a display;

an input device; and

5 a processor coupled to said scanning device, said display and said input device, said processor controlling said scanning device to scan the manufacturing device, said processor displaying on said display a plurality of images corresponding to respective areas of the manufacturing device, said processor inputting at least two selected images from the input device, deriving a spatial relationship between the

10 selected images and forming a pattern to be recognized on the manufacturing device from the selected images and the derived spatial relationship.

25. The apparatus of claim 24 further comprising:

a computer readable storage medium coupled to said processor, wherein

15 said processor stores information associated with the selected images and the derived spatial relationship on said computer readable medium.

26. The apparatus of claim 25 wherein said stored selected images and the stored derived spatial relationship are used by the processor in a pattern

20 recognition analysis to detect defects in the manufacturing device.

27. The apparatus of claim 25 wherein said stored selected images and the stored derived spatial relationship are used by the processor in a pattern

recognition analysis to detect desired patterns in the manufacturing device.

28. The apparatus of claim 24 wherein said processor inputs selected images by displaying on said display at least two image selection windows with the displayed images and inputs data corresponding to images selected by the selection windows via said input device.

29. The apparatus of claim 28 wherein a user of the apparatus is prompted for a number of images windows to be displayed.

10       30. The apparatus of claim 24 wherein said processor inputs selected images by displaying an image selection window on said display with the displayed images, inputs data corresponding to images selected by the image selection window via said input device, determines if another image selection window is required, and if another image selection window is required, continues to display an image selection window, input data and determines if another window is required until no further windows are required.

15       31. The apparatus of claim 24 wherein the manufacturing device is a semiconductor wafer.

20       32. The apparatus of claim 31 wherein the areas are contact areas formed in the wafer.

33. The apparatus of claim 24 wherein the manufacturing device is a reticle.

34. The apparatus of claim 24 wherein the derived spatial relationship  
5 comprises respective spatial relationships between pairs of the selected images.

~~35. An inspection apparatus for use in inspecting a semiconductor wafer, said apparatus comprising:~~

a scanning device, said scanning device obtaining images of a wafer;

10 a display;

an input device; and

15 a processor coupled to said scanning device, said display and said input device, said processor controlling said scanning device to scan a wafer, said processor displaying on said display a plurality of images corresponding to areas of the scanned

wafer, said processor inputting at least two selected images from the input device, deriving a relationship between the selected images and forming a pattern to be recognized on the scanned wafer from the selected images and the derived relationship.

20 36. The apparatus of claim 35 further comprising:

a computer readable storage medium coupled to said processor, wherein said processor stores information associated with the selected images and the derived relationship on said computer readable medium.

37. The apparatus of claim 36 wherein said stored selected images and the stored derived relationship are used by the processor in a pattern recognition analysis to detect defects in the scanned wafer.

5           38. The apparatus of claim 36 wherein said stored selected images and the stored derived relationship are used by the processor in a pattern recognition analysis to detect desired patterns in the scanned wafer.

~~39.~~ An inspection apparatus for use in inspecting a semiconductor wafer,  
10       said apparatus comprising:  
                a scanning device, said scanning device obtaining images of a wafer;  
                an input device; and  
                a processor coupled to said scanning device and said input device, said  
processor controlling said scanning device to scan a wafer, said processor inputting at  
15       least two selected images from the input device and forming a pattern to be  
recognized on the scanned wafer from the selected images and a spatial relationship  
between the images.

ABSTRACT

A pattern inspection apparatus and method that uses multiple images in a pattern recognition process used to detect defects in an object being inspected is disclosed. A user is provided with multiple image selection windows allowing the 5 user to select multiple desired images from the object to form a pattern to be recognized within the object. The multiple desired images will be substantially free from undesired features of the object. Once the multiple desired images are selected, the spatial relationship between them is determined and used to learn the pattern to be recognized. The spatial relationship between the desired images 10 further filters out undesired features. The pattern to be recognized is used in a subsequent pattern recognition analysis. Since the pattern to be recognized includes only desired images and their relationship, undesired features that could corrupt the pattern recognition analysis are not present during the analysis.

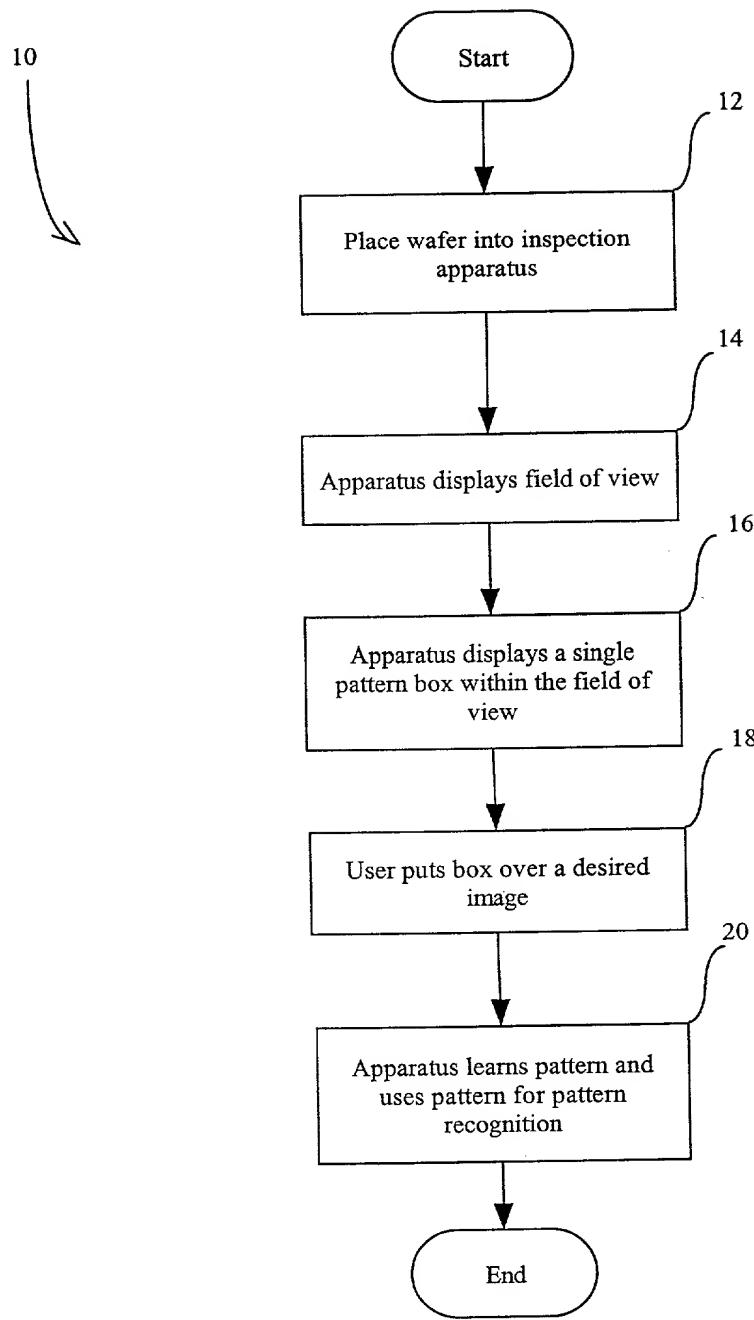
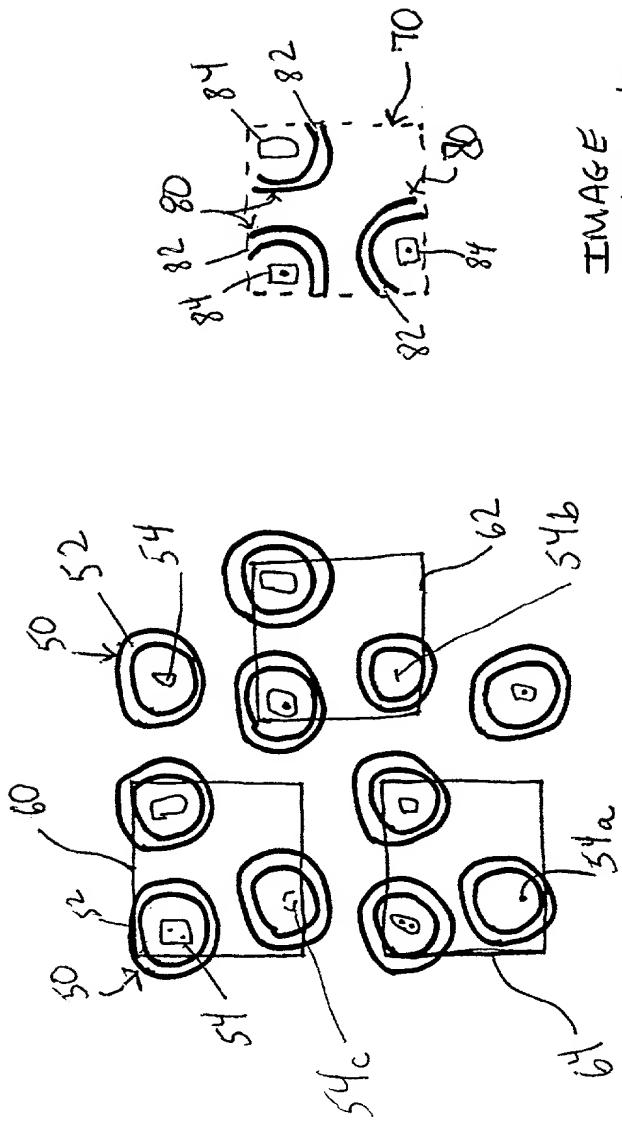


Fig. 1  
(PRIOR ART)

Fig. 2

IMAGE  
Pattern to  
Recognize

Inspected Images



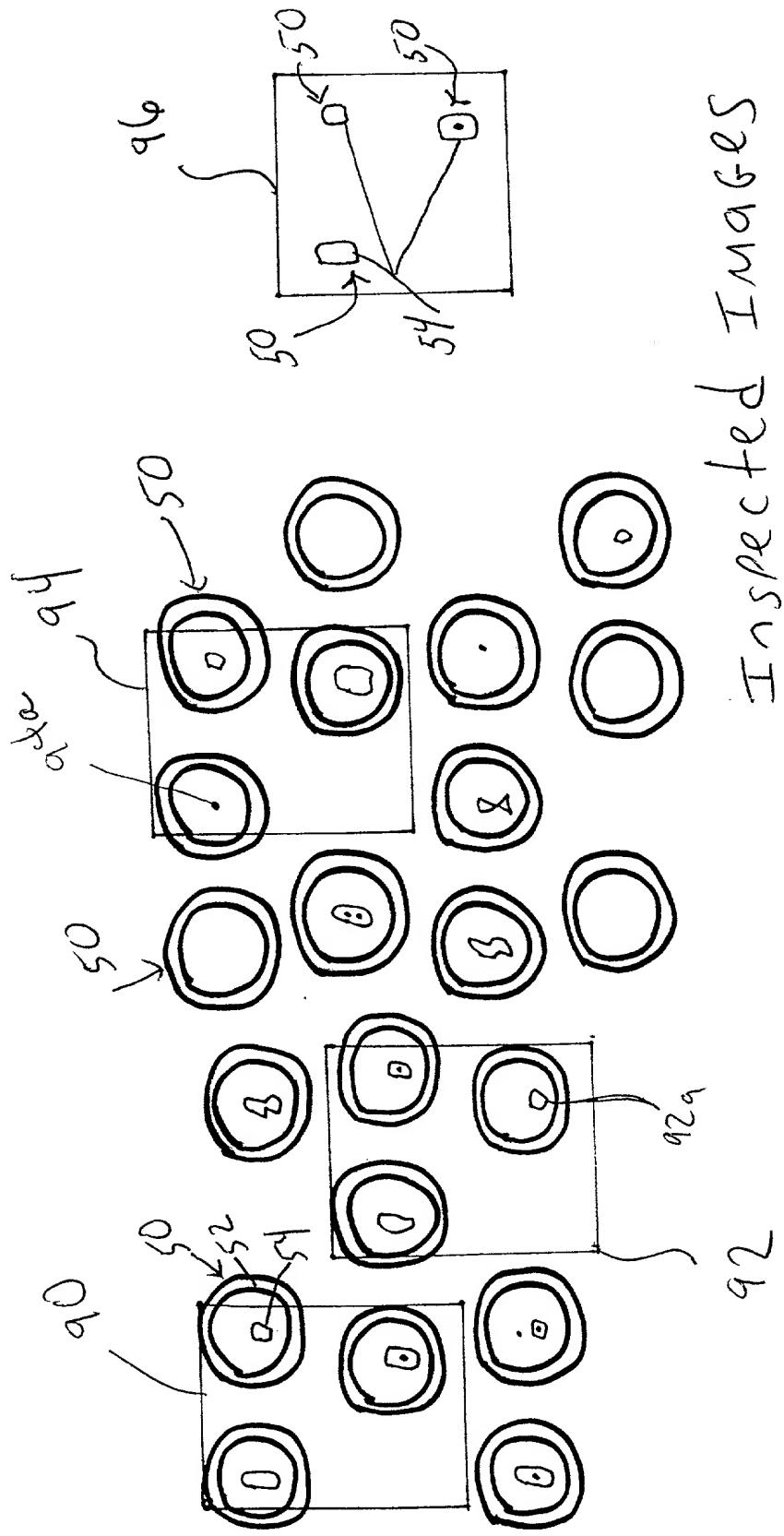


Fig. 3

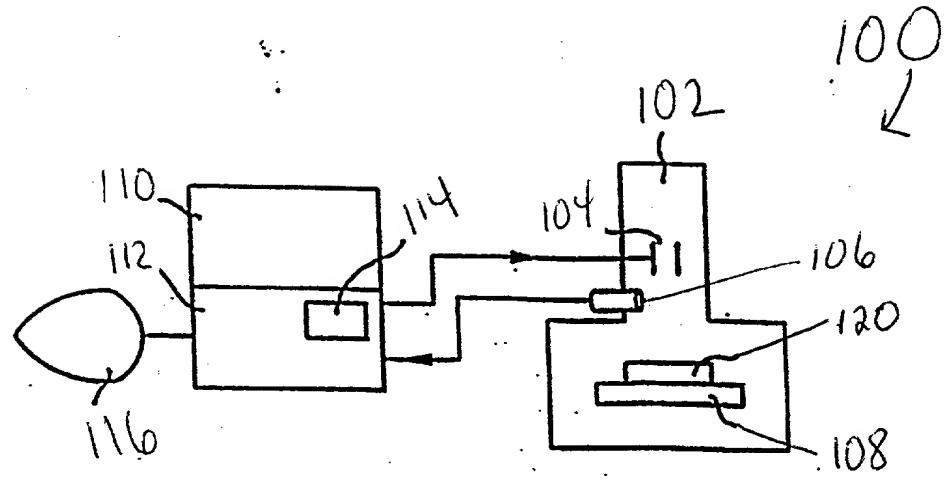


Fig. 4

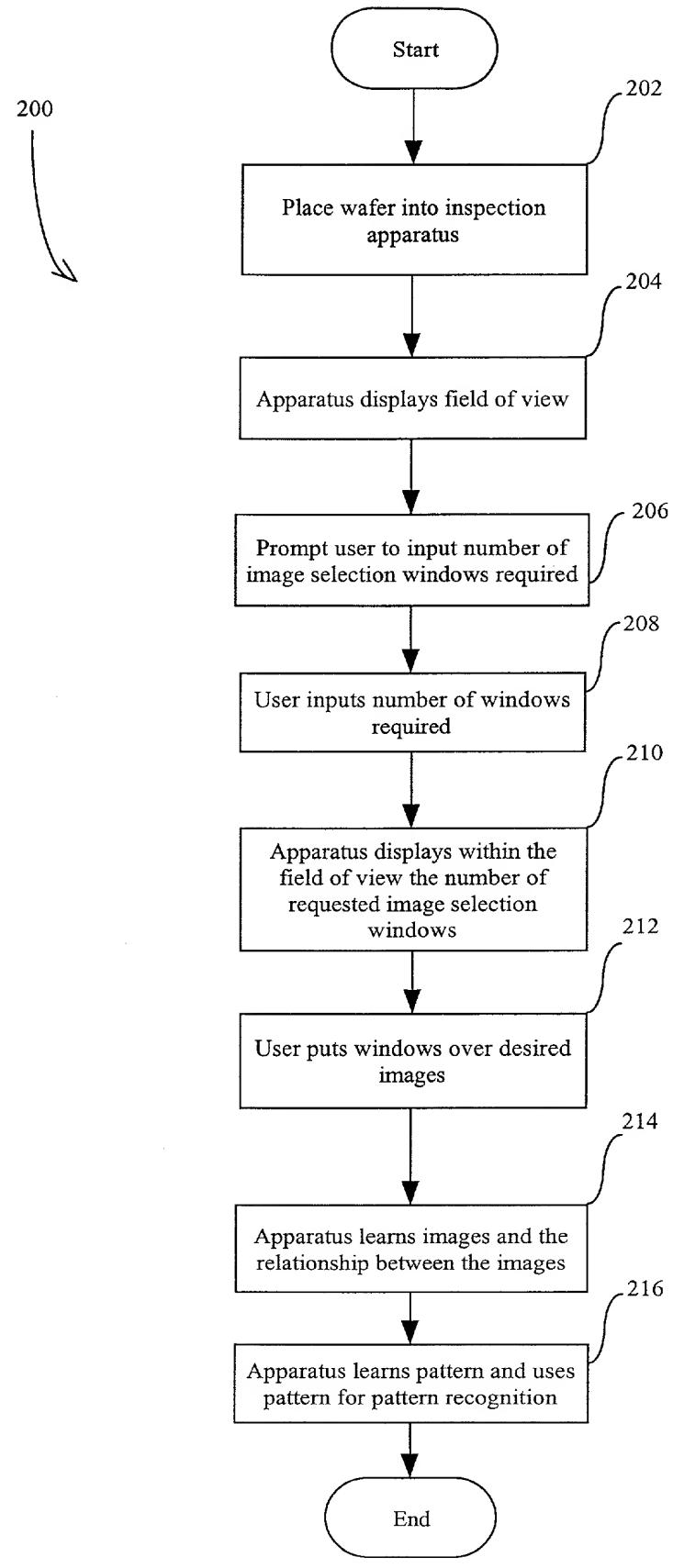


Fig. 5

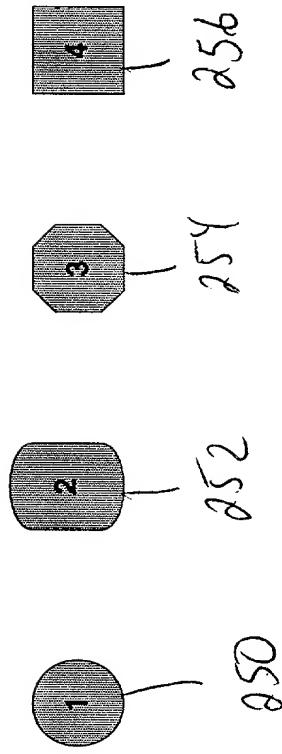


Fig. 6a

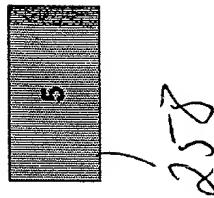


Fig. 6b

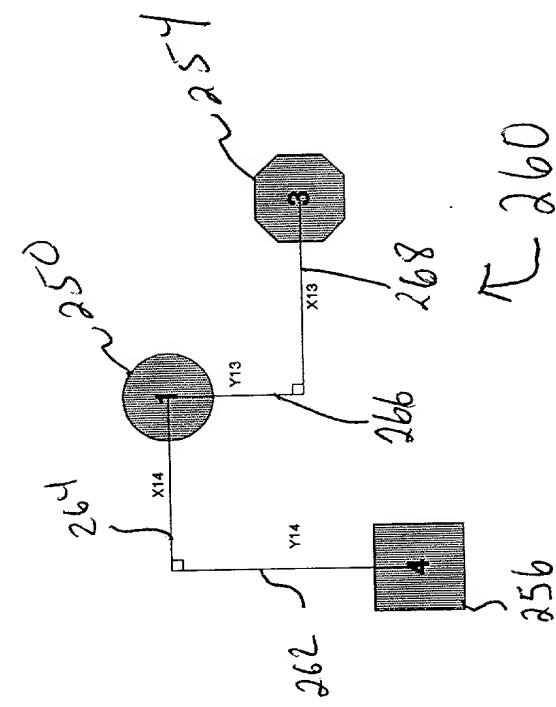
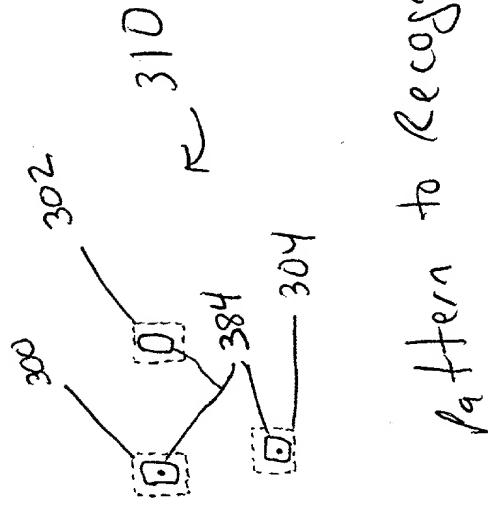
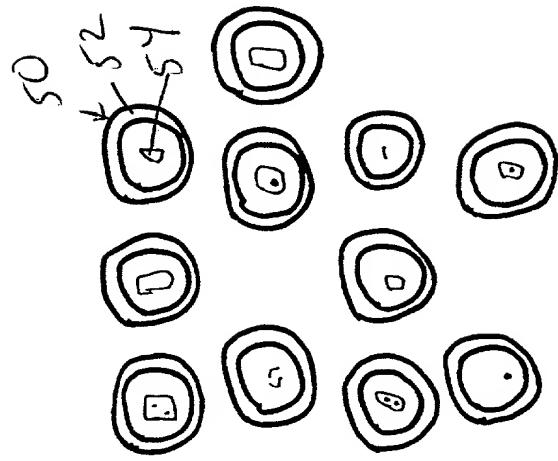


Fig. 6c



Pattern to Recognize

Inspected Images

Fig. 7

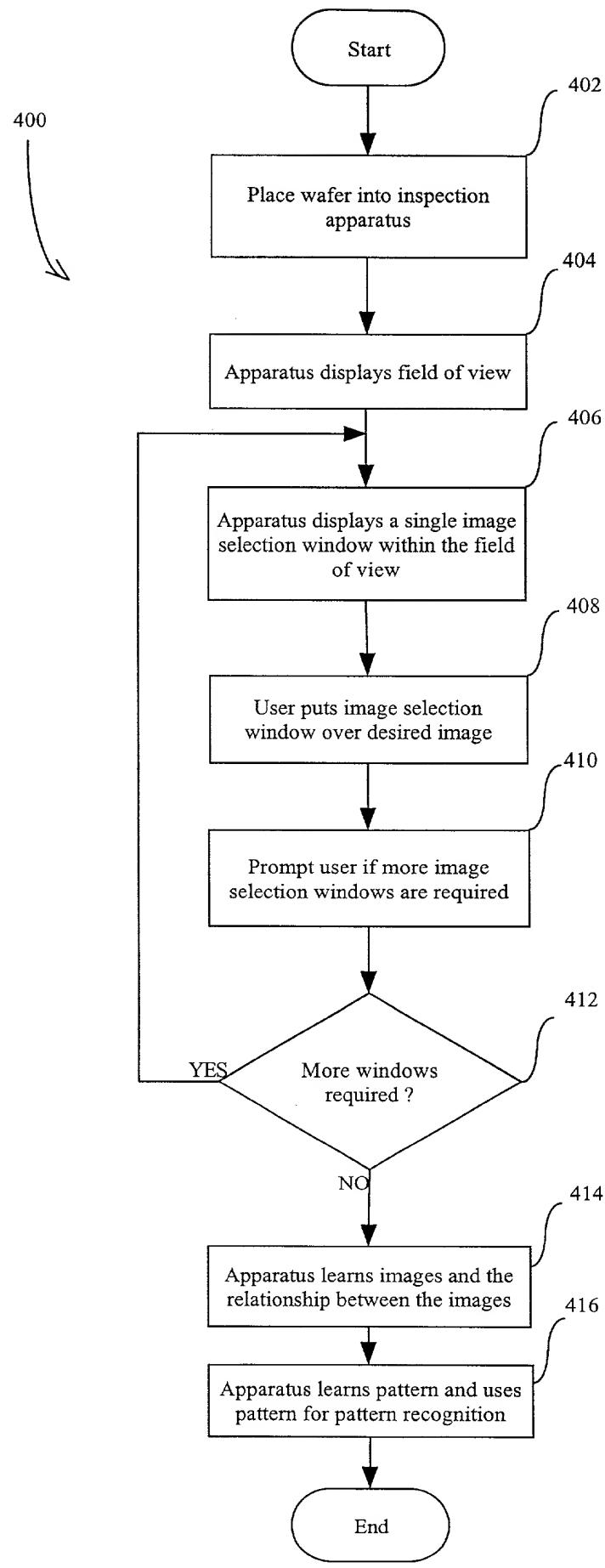
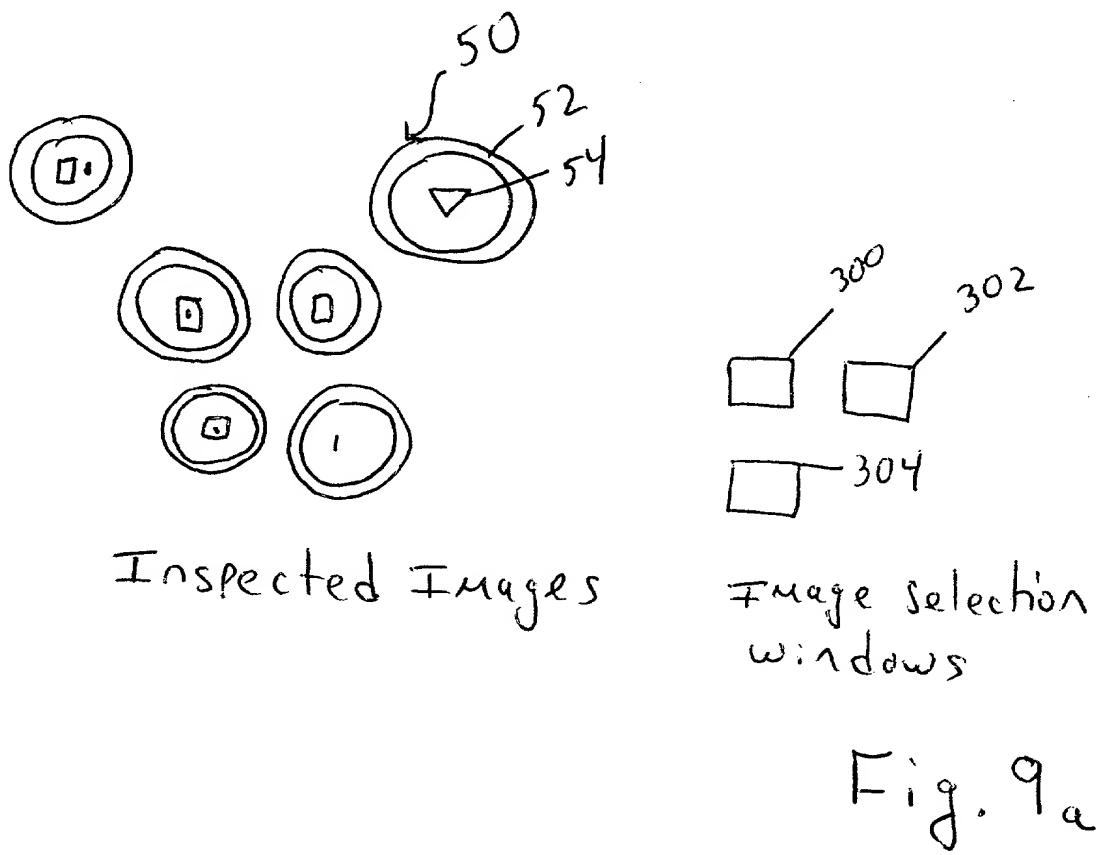
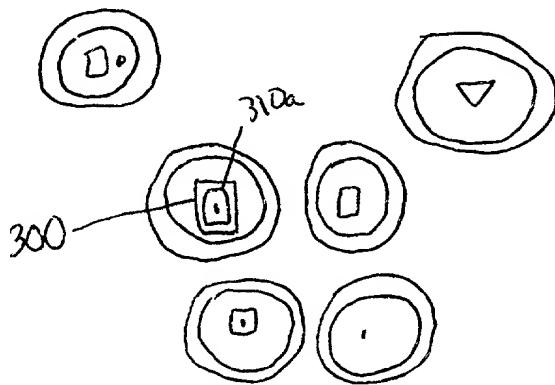


Fig. 8





Inspected Images  
(one image selected)

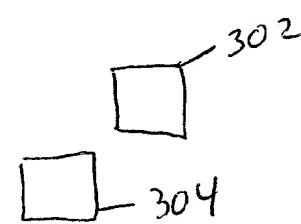
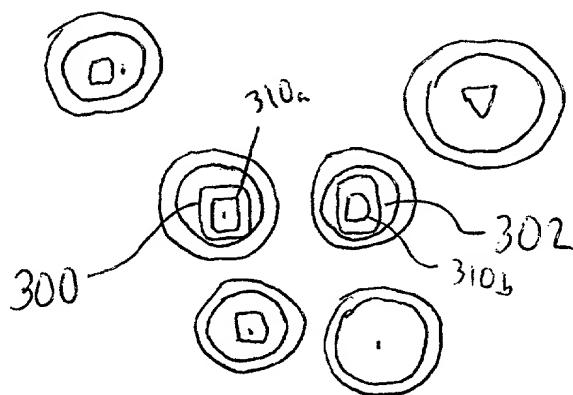


Image Selection  
Windows  
(Remaining)

Fig. 9 b



Inspected Images  
(Two Images selected)

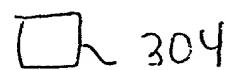
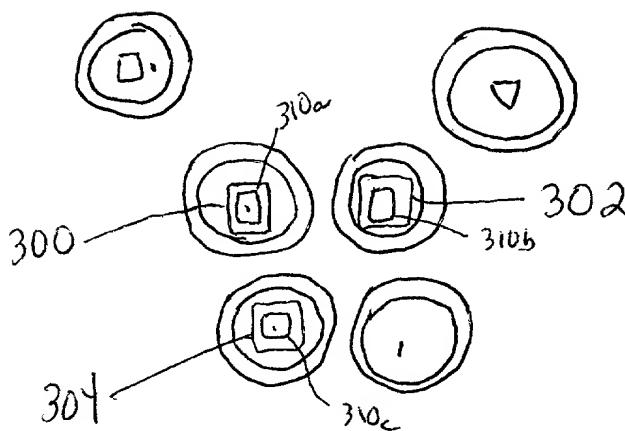


IMAGE Selection  
Window (Remaining)

Fig. 9 c.



Inspected Images  
(Three Images Selected)

Fig. 9d

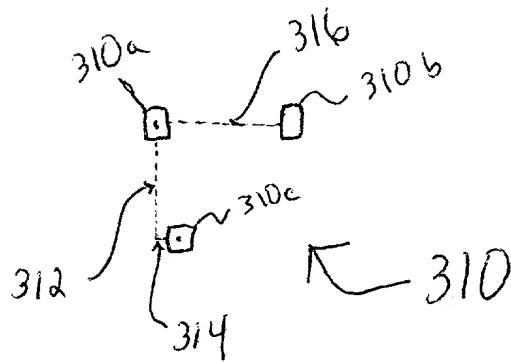


Fig. 9e

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**DECLARATION FOR PATENT APPLICATION**

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled:

**PATTERN RECOGNITION WITH THE USE OF MULTIPLE IMAGES.**

The specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by an amendment, if any, specifically referred to in this oath or declaration.

I acknowledge the duty to disclose all information known to me which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119/365 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

None

I hereby claim the benefit under Title 35, United States Code, § 120/365 of any United States and PCT international application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56(a) which became available between the filing date of the prior application and the national or PCT international filing date of this application:

None

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Please address all correspondence to Thomas J. D'Amico of Dickstein Shapiro Morin & Oshinsky LLP located at 2101 L Street, NW, Washington, DC 20037-1526. Telephone calls should be made to Thomas J. D'Amico by dialing (202) 828-2232.

Full name of sole inventor: Douglas D. Do

Inventor's signature Douglas D. Do Date 1-21-2000

Residence: Boise, Idaho

Citizenship: United States

Post Office Address: 1730 S. Linda Vista Ave  
Boise, ID 83907

PATENT  
Docket No.: M4065.0225/P225  
Micron Ref. No.: 99-0079

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Douglas D. Do

Serial No.: Not yet Assigned

Group Art Unit: Not yet assigned

Filed: Herewith

Examiner: Not yet Assigned

For: PATTERN RECOGNITION WITH  
THE USE OF MULTIPLE IMAGES

Assistant Commissioner for Patents  
Washington, D.C. 20231

**POWER OF ATTORNEY BY ASSIGNEE AND**  
**CERTIFICATE BY ASSIGNEE UNDER 37 CFR § 3.73(b)**

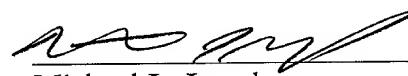
Micron Technology, Inc., assignee of the entire right, title and interest in the above-identified application by virtue of the assignment attached hereto (which is also being submitted concurrently for recordation), hereby appoints the attorneys and agents of the firm of Dickstein Shapiro Morin & Oshinsky LLP located at 2101 L Street, NW, Washington, DC 20037-1526, listed as follows: Gary M. Hoffman, 26,411; Thomas J. D'Amico, 28,371; Donald A. Gregory, 28,954; James W. Brady, Jr., 32,115; Jon D. Grossman, 32,699; Mark J. Thronson, 33,082; Laurence D. Fisher, 37,131; Brian A. Lemm, 43,748; Gianni Minutoli, 41,198; Eric Oliver, 35,307; William E. Powell, III, 39,803; Edwin Oh, 45,319; and Salvatore P. Tamburo, 45,153; and also attorneys Michael L. Lynch, 30,871; Lia M. Pappas, 34,095; W. Eric Webostad, 35,406; and Charles B. Brantley, II, 38,086 of Micron Technology, Inc. as its attorneys with full power of substitution to prosecute this application and to transact all business in the Patent and Trademark Office in connection therewith.

The assignee certifies that the above-identified assignment has been reviewed and to the best of the assignee's knowledge and belief, title is in the assignee.

Please direct all correspondence regarding this application to the following:

Thomas J. D'Amico, Esq.  
Dickstein Shapiro Morin & Oshinsky LLP  
2101 L Street, NW  
Washington, D.C. 20037-1526  
Telephone: (202) 828-2232  
Facsimile: (202) 887-0689

MICRON TECHNOLOGY, INC.



Michael L. Lynch  
Chief Patent Counsel  
Registration No. 30,871

Dated: 1-25-00